



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/334,646

Filed: June 17, 1999

For: SEMICONDUCTOR DEVICE AND

FABRICATION METHOD THEREOF

) Group Art Unit: 2811

) Examiner: S. Hu

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Complete if Known

Application Number	09/334,646
Filing Date	June 17, 1999
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2811
Examiner Name	S. Hu
Attorney Docket Number	0756-1984

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
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		EP	0 657 863			06/14/1995		Eng.
		JP	58-127318			07/29/1983		Abst.
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Examiner Signature					Date Considered			

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Group Art Unit	2811
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Attorney Docket Number	0756-1984

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	04-282869			10/07/1992		Abst.
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		I. Fujieda et al., <i>Self-Referenced Poly-Si TFT Amplifier Readout for a Linear Image Sensor</i> , IEDM '93, Proc., 1993, Pages 587-590.	
		M. Bonnel et al., <i>Polycrystalline Silicon Thin-Film Transistors with Two-Step Annealing Process</i> , IEEE Electron Device Letters, Vol. 14, No. 12, December 1993, Pages 551-553.	
		M. Fuse et al., <i>Performance of Poly-Si Thin Film Transistors Fabricated by Excimer-Laser Annealing of SiH₄- and Si₂H₆- Source Low Pressure Vapor Deposited a-Si Films With or Without Solid-Phase Crystallization</i> , Solid State Phenomena, Vols. 37-38, 1994, Pages 565-570.	

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